

FIG. 1 (PRIOR ART)

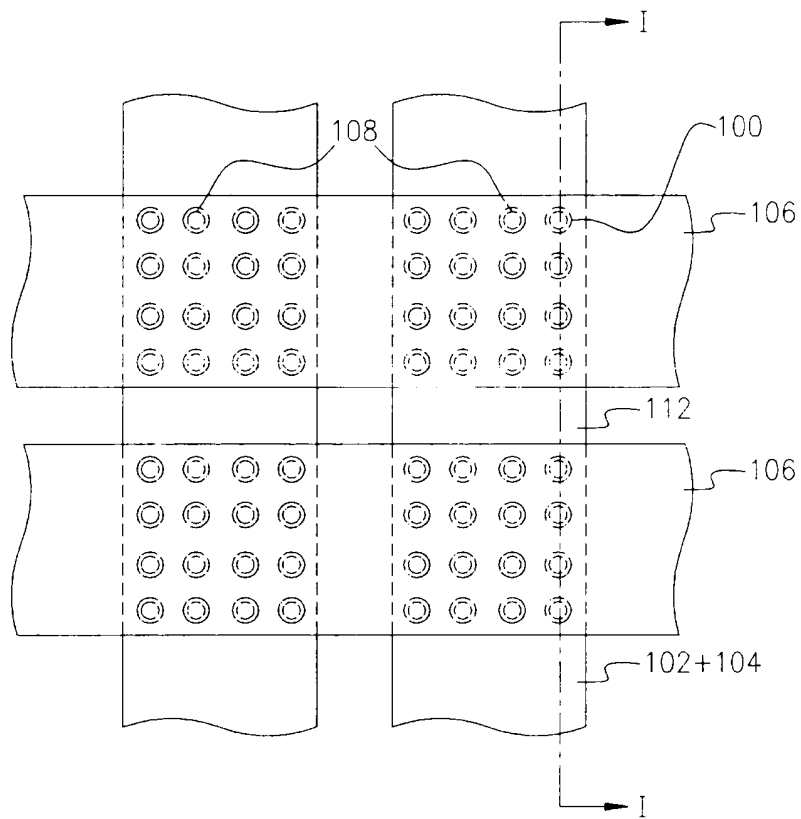


FIG. 2A (PRIOR ART)

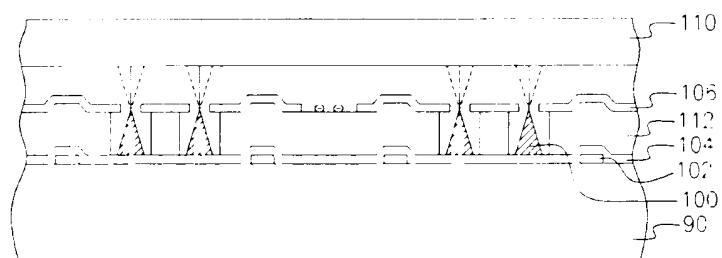


FIG. 2B (PRIOR ART)

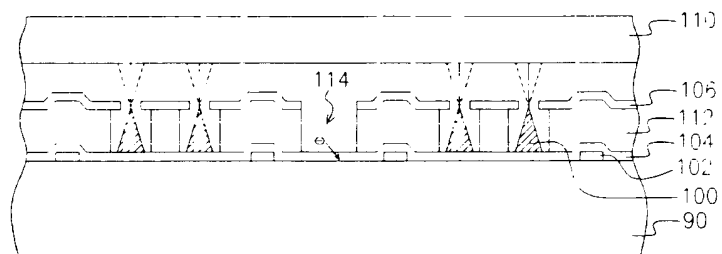


FIG.

FIG. 1 is a cross-sectional view of a device 100 in accordance with one embodiment of the present invention. The device 100 includes a substrate 90, a layer 100, a patterned layer 102, a layer 104, a layer 112, and a top layer 106. A central region 110 is defined by a dashed line. A feature 114 is shown within the central region, and a small gap or feature is labeled 'a'.